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APPLICATION NO.		FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/894,950	09/894,950 06/27/2001		Shunpei Yamazaki	07977/280001/US5027	6263	
26171	7590	03/23/2006		EXAM	EXAMINER	
FISH & RI P.O. BOX 1		DSON P.C.	EVERHART, CARIDAD			
		N 55440-1022		ART UNIT	PAPER NUMBER	
	ŕ			2891		
				DATE MAILED: 03/23/2006		

Please find below and/or attached an Office communication concerning this application or proceeding.

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		09/894,950	YAMAZAKI ET A	L.
	Office Action Summary	Examiner	Art Unit	1
		Caridad M. Everha	t 2891	
Period for	- The MAILING DATE of this commur Reply	nication appears on the cover s	heet with the correspondence a	ddress
WHIC - Extensions after S - If NO - Failure Any re	PRTENED STATUTORY PERIOD F HEVER IS LONGER, FROM THE N sions of time may be available under the provisions BIX (6) MONTHS from the mailing date of this come period for reply is specified above, the maximum st e to reply within the set or extended period for reply ply received by the Office later than three months of patent term adjustment. See 37 CFR 1.704(b).	MAILING DATE OF THIS CON s of 37 CFR 1.136(a). In no event, howeve nunication. latutory period will apply and will expire SIX will, by statute, cause the application to be	MUNICATION. r, may a reply be timely filed (6) MONTHS from the mailing date of this scome ABANDONED (35 U.S.C. § 133).	
Status				
1) ズ	Responsive to communication(s) file	ed on 12 January 2006		
· · · · · · · · · · · · · · · · · · ·		2b) ☐ This action is non-final.		
3)	Since this application is in condition closed in accordance with the pract	for allowance except for form	• •	ne merits is
Disposition	on of Claims		·	
5) 🖾 (6) 🖾 (7) 🔲 (Claim(s) <u>1-78</u> is/are pending in the above claim(s) is/a Claim(s) <u>11-20,32-46,55-59,62,64,6</u> Claim(s) <u>1-10,21-31,47-54,60,61,63</u> Claim(s) is/are objected to. Claim(s) are subject to restrict	re withdrawn from considerati 66,67,70,72,74,75 and 78 is/an 8,65,68,69,71,73,76 and 77 is/	e allowed. are rejected.	
Application	on Papers			
9)□ T	he specification is objected to by th	e Examiner.		
10)□ T	he drawing(s) filed on is/are	: a) ☐ accepted or b) ☐ object	ted to by the Examiner.	
	Applicant may not request that any obje	*	* * * * * * * * * * * * * * * * * * * *	
	Replacement drawing sheet(s) including he oath or declaration is objected to			
Priority u	nder 35 U.S.C. § 119			
12) A A	Acknowledgment is made of a claim All b) Some * c) None of: Certified copies of the priority Certified copies of the priority	documents have been received documents have been received of the priority documents have been Bureau (PCT Rule 17.2(a)	ed. ed in Application No e been received in this Nationa)).	l Stage
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Attachment(s) of References Cited (PTO-892)	∧ □	oniious Cummons (DTO 442)	
	of Draftsperson's Patent Drawing Review (F		erview Summary (PTO-413) per No(s)/Mail Date	
3) 🔲 Inform	ation Disclosure Statement(s) (PTO-1449 or No(s)/Mail Date	PTO/SB/08) 5) 🔲 No	tice of Informal Patent Application (PT ner:	O-152)

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Response to Arguments

Applicant has amended to include the limitation "having a channel formation region" formed over the planarization region.

Applicant's arguments have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 103

The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.

Claims 1-10, 21-31, 47-54, 60,61,63,65,68,69,71,73,76, and 77 are rejected under 35 U.S.C. 103(a) as being unpatentable over Morita et al (US 6,259,200B1) in view of Sato et al (US 6,327,006B1) and further in view of Kim, et al. (US 6,100,954).

Morita et al discloses a device which is formed o an insulating surface which is an insulating substrate (Abstract and col. 8, lines 17-18), a gate line and a shielding film formed on the surface(col. 8, lines 19-24 and col. 2, lines 27-33), in which the disclosure that the gate and shielding film are formed concurrently of the same material discloses that they are formed on the same surface. The shielding film has a planarization film over it(col. 4, lines 30-36) and and ITO film is over the planarization film(col. 4, lines 33-37), and this satisfies that there is a semiconductor film over the planarization film, as ITO is a semiconductor. The thickness of the shielding film and of the gate is 100nm, which is 0.1 micron(col. 6, lines 37-40). In Fig. 6 it is seen that the gate and shielding film are tapered. With respect to the limitation of a capacitance wiring, the lower metal

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of the gate line and the shielding layer can be considered the bottom capacitance wiring and the ITO layer can be considered that top capacitance wiring.

Morita et al is silent with respect to polishing or CMP being the planarization method of the planarization layer nor the channel forming region over the planarization layer nor the recited devices.

Kim, et al discloses a gate 113 with a planarization layer thereon, 157, and a channel formation region over the planarization layer, 119(col. 16, lines 45-65). In addition, Kim, et al disclose in Fig. 10G metallization 127, planarizationlayer 159, and metallization 131(col. 15, lines 44-60).

Although Kim, et al does not teach the shielding film, the function of the shielding film is not required(col. 16, lines 26-30), and the element is obvious if the elimination of the element is because its function is not desired(MPEP 2144.04).

Sato, et al disclose a planarized layer between the TFT and the upper ITO layer which is planarized by CMP(layer 100 shown in Fig. 3 and described in col. 3, lines 3-15).

It would have been obvious to one of ordinary skill in the art at the time of the invention that the channel forming layer could have been formed over the planarization layer in the method taught by Morita et al as is taught by Kim, et al because this would obtain the benefit taught by Kim, et al of eliminating the stepped surface(col. 4,lines 24-28), and improved aperture ratio(col. 16, lines 25-30).

It would have been obvious to one of ordinary skill in the art at the time of the invention to have planarized the planarizing film in the process taught by Morita et al by

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using polishing or CMP because Sato et al teaches that the film in a similar device and because CMP is well known in the art. In addition, in the device claims it is the device which is rejected, and the method of planarization is not given patentable weight in a device claim.

It would have been obvious to one of ordinary skill in the art at the time of the invention to have used the device taught by Morita et al in the recited devices because it is well known in the art that the recited devices include TFTs, and Morita et al teaches TFTs.

Allowable Subject Matter

Claims 11-20, 32-46, 55-59,62,64,66,67,70,72,74,75, and 78 are allowable.

The prior art of record does not teach the TFT on the planarized layer in combination with the other limitations of the allowable claims.

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of

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the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Caridad M. Everhart whose telephone number is 571-272-1892. The examiner can normally be reached on Monday through Fridays 7:30-4:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, B. Baumeister can be reached on 571-272-1722. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

PRIMARY ELV MATE

C. Everhart 3-18-2006